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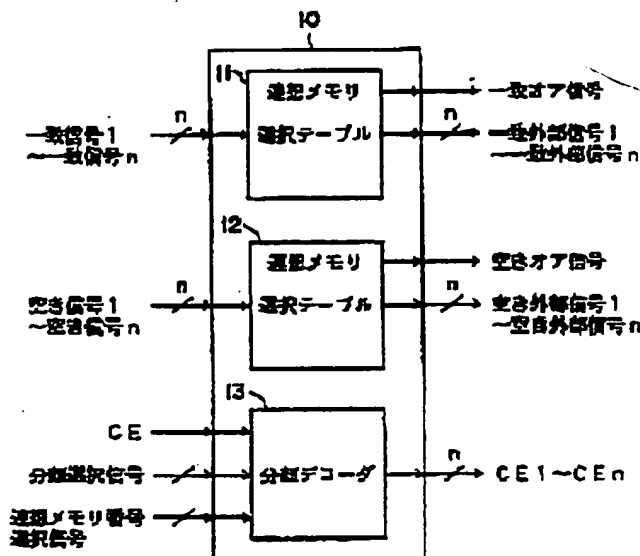
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TITLE : ASSOCIATIVE MEMORY SYSTEM



ABSTRACT : **PURPOSE:** To provide an associative memory system in which a plurality of associative memories can be accessed freely for each group by providing a controller for bringing an associative memory, belonging to a desired group in the plurality of associative memories, into chip enable state.

CONSTITUTION: An associative memory controller 10 comprises an associate memory selection table 11 for coincidence signal, 2 an associate memory selection table 12 for blank signal, and a sorting decoder 13. The table 11 receives coincidence signals 1-n from (n) associate memories and delivers coincidence external signals 1-n and a coincidence OR signal. The sorting decoder 13 receives a chip enable signal CE, a sort selection signal and an associate memory number selection signal to produce a chip enable signal CE1-CEn for each associate memory.

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